Boolean Algebra

TOPIC- 1 Basics of Boolean Algebra Very Short Answer Type Questions (1 Mark each)

Question 1:

Express the OR operator in terms of AND and NOT operator.

Answer:

$$(A \cdot B)' = A + B \qquad [De Morgan's law]$$
$$(\overline{A} + \overline{B})' = A.B.$$
$$A \longrightarrow 0 \quad \overline{\overline{A}}$$
$$B \longrightarrow 0 \quad \overline{B} \quad \longrightarrow 0 \quad A + B$$

Question 2:

Specify which axioms/theorems are being used in the following Boolean reductions:

- (a) (bc)' + bc = 1
- (b) xyz + zx = xz

Answer:

- (a) x+x'=1-Complementary law
- (b) xy+x=x & absorption law

Question 3:

Which gates are known as universal gates? Why?

Answer:

Universal gates are the ones which can be used for implementing any gate like AND, OR and NOT or any combination of these basic gates. NAND and NOR gates are universal gates.

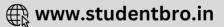
Question 4:

State and verify Associative law using Truth Table.

Answer:

Associative Law: This law states that:





(A+B)+C=A+(B+C) (A.B).C=A.(B.C) **Proof:**

Α	В	С	A + B	(A + B) + C	B + C	A + (B + C)
0	0	0	0	0	0	0
0	0	1	0	1	1	1
. 0	1	0	1	1	1	1
0	1	1	1	1	1	1
1	0	0	1	1	0	1
1	0	1	1	1	1	1
1	1	0	1	1	1	1
1	1	1	1	1	1	1

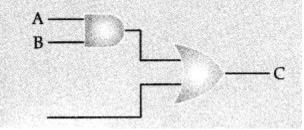
From the above tables, (A+B)+C=A+(B+C)Similarly, we can prove, A.(B.C)=(A.B).C

A	В	с	B.C	A. (B. C)	A.B	(A.B).C
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	1	0	0	0
1	0	0	0	0	0	0
1	0	1	0	0	0	0
1	1	0	0	0	1	0
1	1	1	1	1	1	1

Question 5:

Draw the equivalent logic circuit for the following Boolean experession :

Answer:



Short Answer Type Questions

Question 1:

State and define principle of duality. Why is it so important in Boolean Algebra?





Principle of duality: Duality principle states that from every boolean relation another boolean relation can be derived by:

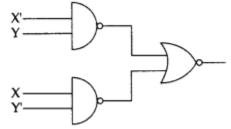
- 1. Changing each OR sign (+) to an AND sign (•)
- 2. Changing each AND sign (.) to an OR sign (+)

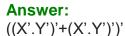
ex : Dual of A + A'B = A . $(A^1 + B)$

Importance in Boolean Algebra: The principle of duality is an important concept in Boolean algebra, particularly in proving various theorems. The principle of duality is used extensively in proving Boolean algebra theorem. Once we prove that an expression is valid, by the principle of duality, its dual is also valid. Hence, our effort in proving various theorems is reduced to half.

Question 2:

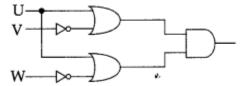
Write the equilent boolean expression for the following logic circuit.





Question 3:

Write the equilent boolean expression for the following logic circuit.



Answer:

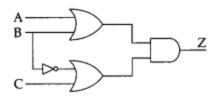
 $Y = (U + \overline{V}) \cdot (U + \overline{W})$

Question 4:

Write the equivalent boolean expression for the following logic circuit:







Answer: Z = (A+B).(B'+C) = A.B' + AC + B.B' + BC

=A.B' + AC + BC

Question 5:

Name the law shown below & verify it using a truth table. $X + \overline{X} \cdot Y = X + Y$.

Answer:

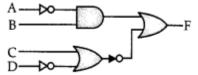
This law is called "Absorption Law" also reffered as redundance law.

Prove by Truth table

x	Y	X'	X+X'.Y	X+Y
0	0	1	0	0
0	1	1	1	1
1	0	0	1	1
1	1	0	1	1

Question 6:

Obtain the Boolean Expression for the logic circuit shown below.



Answer: $F = (\overline{A} \cdot B) + (\overline{C + \overline{D}})$ $= (\overline{A} \cdot B) + \overline{C} \cdot D$

Question 7:

Verify the following using truth table: (i) X+0=X (ii) X+x'=1





(i)

x	0	Y = X + 0
0	0	0
1	0	1

Hence, X + 0 = X

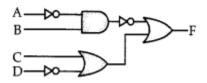
(ii)

x	x	$\mathbf{Y} = \mathbf{X} + \mathbf{X}'$
0	1	1
1	0	1

Hence, X + X' = 1

Question 8:

Obtain the Boolean Expression for the logic circuit shown below.



Answer:

$$F = (\overline{X.\overline{Y}}) + (\overline{Z} + W).$$

$$F = \overline{X} + Y + \overline{Z} + W.$$

Question 9:

State Demorgan's law. Verify one of them using truth table There are two Demorgan's law :

(i)
$$\overline{A.B} = \overline{A} + \overline{B}$$

(ii) $\overline{A+B} = \overline{A} \cdot \overline{B}$

Answer:





Proof :

(i) $\overline{A}.\overline{B} = \overline{A} + \overline{B}$

A	В	Ā	B	Ā.B	$\overline{\mathbf{A}} + \overline{\mathbf{B}}$
0	0	1	1	1	1
0	1	1	0	1	1
1	0	0	1	1	1
1	1	0	0	0	0

Proof :

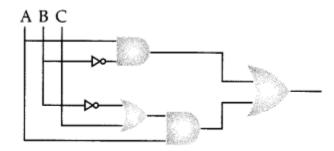
(i) $\overline{A \cdot B} = \overline{A} + \overline{B}$

A	В	Ā	B	Ā.B	$\overline{\mathbf{A}} + \overline{\mathbf{B}}$
0	0	1	1	1	1
0	1	1	0	1	1
1	0	0	1	1	1
1	1	0	0	0	0

Question 10:

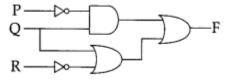
Draw a logic circuit for the Boolean expression A. $\overline{B} \ + (C + \ \overline{B}$). \overline{A} .

Answer:



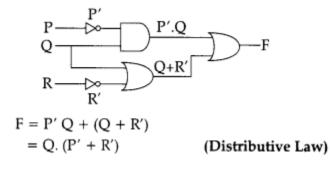
Question 11:

Obtain the boolean expression for the logic circuit shown below:









Question 12:

Verify the following using Boolean Lawas: X+Z=X+X'Z+Y.Z

Answer:

Taking RHS X + X'Z + YZ= $(X + X') \cdot (X + Z) + YZ$ (Distribution Law) = 1 · (X + Z) + YZ (A + A' = 1) = X + Z + YZ = X + Z (1 + Y) = X + Z (1 + Y) = Hence Verified

Question 13:

Verify the following using Boolean Lawas: A+C=A+A'.C+B.C

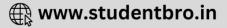
Answer:

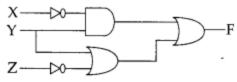
A + C = A + A'.C + BCSolve RHS A + A'C + BC(A + A').(A + C) + BC[Using distributive law] 1. (A + C) + BC = A + C + BC= A + C + BC= A + C(1 + B)= A + C.1 = A + C = L.H.S. (Hence, verified)

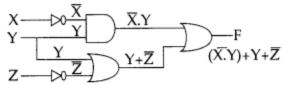
Question 14:

Obtain the Boolean Expression for the logic circuit shown below:









Expression at F :

$(X \cdot Y) + (Y + Z)$	
\Rightarrow (\overline{X} + 1) Y + \overline{Z}	[Distributive law]
\Rightarrow Y + \overline{Z}	$[::1+\overline{Z} = 1] 2$

Question 15:

Verify the following using truth table: (i) X.X'=0 (ii) X+1=1

Answer:

(i)

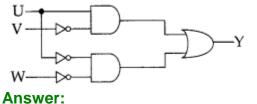
x	X'	Y = X.X'
0	1	0
1	0	0

(ii)

x	1	Y=X+1
0	1	1
1	1	1

Question 16:

Write the equivalent boolean expression for the following logic circuit:



 $Y = U \overline{V} + \overline{U} \overline{W}$





Question 17:

Answer:

Distribution law: This law states that (i) x(y+z)=x.y+x.z(ii) x+yz=(x+y)(x+z)Noe let us prove using truth table.

(i)

(ii)

x	y	z	y + 2	x(y + z)	xy	x.z	x.y + x.z
0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0
0	1	0	1	0	0	0	0
0	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0
1	0	1	1	1	0	1	1
1	1	0	1	1	1	0	1
1	1	1	1	1	1	1	1

Hence
$$x(y + z) = n.y. n.z.$$

	(<i>.</i>					
x	y	z	y.z	x+y.z	x+y	x+z	(x+y)(x+z)
0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0
0	1	0	0	0	1	0	0
0	1	1	1	1	1	1	1
1	0	0	0	1	1	1	1
1	0	1	0	1	1	1	1
1	1	0	0	1	1	1	1
1	1	1	1	1	1	1	1

Hence x + y.z = (x + y).(x + z)

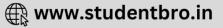
Question 18:

Name the law shown below & verify it using a truth table. A+B.C=(A+B).(A+C).

Answer:

This law is called "Distribute Law".





Prove ny Truth table

Α	B	C	A+B.C	A+B	A+C	(A+B).(A+C)
0	0	0	0	0	0	0
0	0	1	0	0	1	0
0	1	0	0	1	0	0
0	1	1	1	1	1	1
1	0	0	1	1	1	1
1	0	1	1	1	1	1
1	1	0	1	1	1	1
1	1	1	1	1	1	1

Question 19:

Write the POS form of a Boolean Function F, which is represented in a truth table as follows:

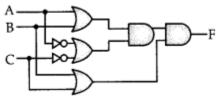
Р	Q	R	F
0	0	0	0
0	0	1	1
0	1	۰.0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Answer:

(P+Q+R).(P'+Q+R).(P'+Q'+R)

Question 20:

Write the Boolean Expression for the result of the Logic Circuit as shown below:



Answer:

The boolean expression is : I=[(A+b).(A'+C')].(B+C)





Question 21:

Derive a Cannonical POS expression for a Boolean function G, represented by the following truth table:

	_		
Х	Y	Z	G (X, Y, Z)
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

Answer:

POS = x'y'z' + x'yz' + x'yz + xy'z + xyz' $= \Sigma m (0, 2, 3, 5, 6)$

Question 22:

Correct the following boolen statements:

(i) X + 1 = X(ii) (A')' = A(iii) A + A' = 0(iv) (A + B)' = A'.B'

Answer:

(i) X + 1 = X or X + 0 = X(ii) ((A')') = A(iii) $A + A' = 1 \text{ or } A \cdot A' = 0$ (iv) $(A + B)' = A' \cdot B'$

Question 23:

Derive a Cannonical SOP expression for a Boolean function G, represented by the following truth table:







Α	В	С	G (A, B, C)
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

G(A, B, C) = A'.B'C' + A'.B.C' + A.B.C' + A.B.COR G(A, B, C) = S(0, 2, 6, 7)

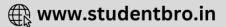
Question 24:

Derive a Canonical POS expression for a Boolen function F, represented by the following truth table:

Р	Q	R	F (P, Q, R)
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1 .	1

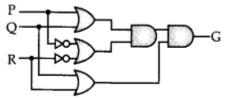
Answer:

$$\begin{split} F & (P, Q, R) = (P' + Q' + R') \cdot (P + Q' R') \cdot (P + Q' R') \\ R'). & (P' + Q + R') \\ OR \\ F & (P, Q, R) = \pi (0, 3, 4, 5) \end{split}$$



Question 25:

Write the Boolen Expression for the result of the Logic Circuit shown below:



Answer: C=P.Q'+P.R+Q.R'

Question 26:

Verify the following using Boolen Laws P'+Q'.R=P'.Q'.R'+P'.Q.R'+P'.Q.R+P'.Q'.R+P.Q'.R

Answer:

LHS P' + Q' R = P' (Q + Q') (R+R') + Q'R (P + P') (Simplification) = P'QR + P'QR' + P'Q'R + P'Q'R' + PQ'R + P'Q'R = P'QR + P'QR' + P'Q'R + P'Q'R' + PQ'R = P'Q'R' + P'Q'R' + P'QR + P'Q'R + PQ'R = R.H.S. 2

Question 27:

Verify the following using Boolen Laws x'+y'z = x'y'z'+x'y'z'+x'yz+x'y'z+xy'z

Answer:

LHS $X' + Y' \cdot Z$ $= X' \cdot (Y + Y') \cdot (Z + Z') + (X + X') \cdot Y' \cdot Z$ $= X' \cdot Y \cdot Z + X' \cdot Y \cdot Z' + X' \cdot Y' \cdot Z + X' \cdot Y' \cdot Z' + X.$ $Y' Z + X' \cdot Y' \cdot Z$ $= X' \cdot Y \cdot Z + X' \cdot Y \cdot Z' + X' \cdot Y' \cdot Z + X' \cdot Y' \cdot Z' + X.$ $Y' \cdot Z$ $= X' \cdot Y \cdot Z + X' \cdot Y \cdot Z' + X' \cdot Y' \cdot Z + X' \cdot Y' \cdot Z' + X.$ $Y' \cdot Z$ = RHS $Y' \cdot Z + X' \cdot Y \cdot Z + X' \cdot Y' \cdot Z + X' \cdot Y' \cdot Z' + X.$



OR RHS $X' \cdot Y' \cdot Z' + X' \cdot Y \cdot Z' + X' \cdot Y \cdot Z + X' \cdot Y' \cdot Z + X \cdot Y' \cdot Z$ $= X' \cdot Y' \cdot Z + X' \cdot Y' \cdot Z' + X' \cdot Y \cdot Z + X' \cdot Y \cdot Z' + X \cdot Y' \cdot Z$ $= X' \cdot Y' \cdot (Z + Z') + X' \cdot Y \cdot (Z + Z') + X \cdot Y' \cdot Z$ $= X' \cdot Y' \cdot (Z + Z') + X \cdot Y' \cdot Z$ $= X' \cdot (Y' + Y) + X \cdot Y' \cdot Z$ $= X' \cdot (Y' + Y) + X \cdot Y' \cdot Z$ $= (X' + X) \cdot (X' + Y' \cdot Z)$ $= X' + Y' \cdot Z$ = LHS

Question 28:

State and prove De-Morgan's laws in boolean algebra. DeMorgan's theorem states that: (i) (X+Y)'=X'.Y' (ii) (X.Y)'=X'+Y'

Answer:

Now ,let us proove using truth table

(i)
$$(X + Y)' = X'.Y'$$

x	Y	X+Y	(X+Y)'	X	Ŷ	X.Y
0	0	0	1	1	1	1
0	1	1	0	1	0	0
1	0	1	0	0	1	0
1	1	1	0	0	0	0

Hence, (X + Y)' = X'.Y'

(ii) (X.Y)' = X' + Y'

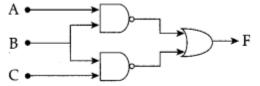
x	Y	X.Y	(X.Y)'	X	Y	X'+Y'
0	0	0	1	1	1	1
0	1	0	1	1	0	1
1	0	0	1	0	1	1
1	1	1	0	0	0	0
-				-	-	

Hence,
$$(X.Y)' = X' + Y$$

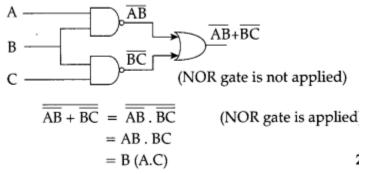
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Question 29:

Write the equivalent boolean expression for the following logic circuit:



Answer:



Question 30:

Verify the following using truth table:

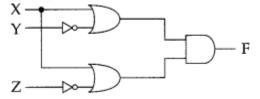
Answer:

 $U.(U' + V) = (U \cdot V)$

U	v	U′	U'+V	U.(U'+V)	U.V
0	0	1	1	0	0
0	1	1	1	0	0
1	0	0	0	0	0
1	1	0	1	1	1

Question 31:

Write the equivalent boolean expression for the following logic circuit:



Answer: F=(X+Y').(X+Z')





Question 32:

Verify the following using truth table: X+Y.Z=(X+Y).(X+Z)

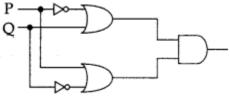
Answer:

This law is called Distributive Law.

x	Y	Z	X+Y	X+Y.Z	X+Z	(X+Y)(X+Z)
0	0	1	0	0	1	0
0	1	0	1	0	0	0
0	1	1	1	1	1	1
1	0	0	1	1	1	1
1	0	1	1	1	1	1
1	1	0	1	1	1	1
1	1	1	1	1	1	1

Question 33:

Write the equivalent boolean expression for the following logic circuit:



Answer:

PQ'+PR' =P(Q'+R')

Question 34:

State abd cerify De-Morgan's laws algebraically

Answer:

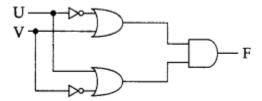
X + X.Y = XL.H.S. = X+X.Y = X.1+X.Y = X.(1+Y) = X.1 = X = R.H.S. X + X'.Y = X+Y L.H.S. = X+X'.Y = (X+X').(X+Y) = 1.(X+Y) = X+Y = R.H.S.





Question 35:

Write the equivalent boolean expression for the following logic circuit:



Answer: F(P,Q)=(P'+Q).(P+Q')

Question 36:

State and algebraically verify Absorption law

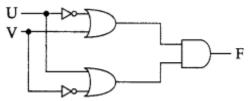
Answer:

$$X + X.Y = X$$

L.H.S. = X+X.Y
= X.1+X.Y
= X.(1+Y)
= X.1
= X = R.H.S.
X + X'.Y = X+Y
L.H.S. = X+X'.Y
= (X+X').(X+Y)
= 1.(X+Y)
= X+Y = R.H.S.

Question 37:

Write the equivalent boolean expression for the following logic circuit:



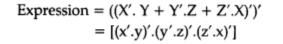
Answer: F(U,V)=(U'+V).(U+V')

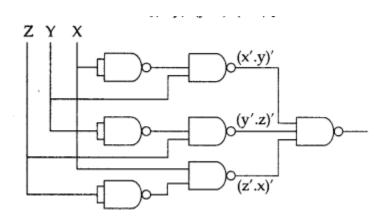
Question 38:

Represent the expression X'.Y+Y'.Z+Z'X using NAND gates



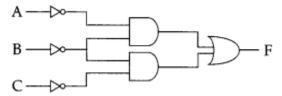






Question 39:

Write the equivalent expression for the following logical circuit:



Answer:

F=A'.B'+B'C'

Question 40:

Verify the following algebraically. (A'+B').(A+B)=A'.B+A.B'

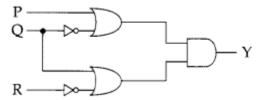
Answer:

(A'+B').(A+B) = A'B + A.B'L.H.S. (A' + B'). (A + B)A'A + A'B + B'A + B'B0 + A'.B + B'.A + 0A'.B + A.B' = R.H.S. (Hence, verified)



Question 41:

Write the equivalent boolean expression for the following logic circuit:



Answer: Y=(P+Q').(Q+R')

Question 42:

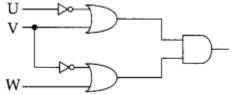
Verify the following algebrically: X'.Y+X.Y'=(X'+Y').(X=Y)

Answer:

$$\begin{array}{l} X'.Y+X.Y' = (X'+Y').(X+Y) \\ R.H.S. = (X'+Y').(X+Y) \\ = X'X+X'Y+Y'X+Y'Y \\ = 0 + X'Y+Y'X+0 \qquad (Q AA'=0) \\ = X'Y+Y'X \qquad (Q A+0=A) \\ = X'Y+XY' \qquad (Q AB'=B'A) \\ = L.H.S. \qquad 2 \end{array}$$

Question 43:

Write the equivalent boolean expression for the following logic circuit:



Answer: (U'+V).(V'+W)

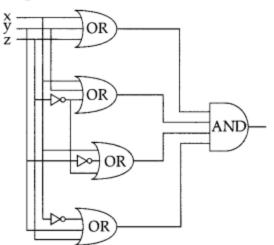
Question 44:

Answer:

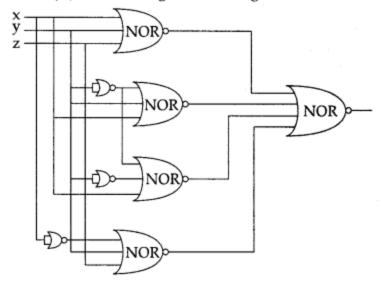




- $\pi (0, 1, 3, 4) = (x + y + z) . (x + y + z').$ (x + y' + z') . (x' + y + z). .
 - (i) Converted to POS
 - (ii) Draw circuit diagram using normal (basic gates).



(iii) Convert all gates to NOR gates.







TOPIC-2

Karnaugh Map Minimization and Applications of Boolean Algebra Very Short Answer Type Questions

Question 1:

Convert the following Boolean expression into its equivalent Canonical Sum of Products form (SOP): (1) (1) (1) (1) (1) (2) (2) (2) (3)

(U + V + W)(U+V + W')(U'+V + W) (U' + V' + W')

Answer:

 $\pi (0, 1, 4, 7)$ $\Sigma (2, 3, 5, 6)$ 010 011 101 110 = U'VW' + U'VW + UV'W + UVW'

Question 2:

Write Product of Sum expression of the function F (a, b, c, d) from the given truth table.

a	b	c	d	f
0	0	0	0	0
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

Answer:

 $\begin{array}{l} F(a,b,c,d) = \\ (a+b+c+d).(a+b+c+d').(a+b'+c+d).\\ (a+b'+c'+d').(a'+b+c+d).\\ (a'+b+c+d').(a'+b'+c+d).(a'+b'+c+d')\\ .(a'+b'+c'+d) & 1 \end{array}$

Question 3:

Write the Sum of Product form of the function F(P, Q, R) for the following truth table represen-tation of F :

Р	Q	R	F
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Answer:

 $(\overline{P} \ \overline{Q} \ \overline{R} + \overline{P} \ QR + PQ \ \overline{R} + PQR.$

Question 4:

Write the Product of Sum form of the function F(X, Y, Z) for the following truth table representation of F :

x	Ŷ	Z	F
0 .	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Answer:

 $(X + Y + \overline{Z}) \cdot (X + \overline{Y} + Z) \cdot (\overline{X} + Y + Z) \cdot (\overline{X} + Y + \overline{Z}).$

Question 5:

Write the Product of Sum form of the function G(U, V, W) for the following truth table representation of G :





U	v	w	G(U, V, W)
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
, 1	0	1	0
1	1	0	1
1	1	1	0

 $\begin{array}{c} (U + V + W) \cdot (U + \overline{V} + W) \cdot (\overline{U} + \overline{V} + \overline{W}) \\ (\overline{U} + \overline{V} + \overline{W}). \end{array}$

Question 6:

Write the Product of Sum form of function G(U, V, W) for the following truth table representation of G :

Maxterm

U	v	W	G
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Answer:

U	v	W	G	POS
0	0	0	1	
0	0	1	0	$U + V + \overline{W}$
0	1	0	1	
0	1	1	0	$U + \overline{V} + \overline{W}$
1	0	0	1	
1	0	1	0	$+ V + \overline{W}$
1	1	0	0	
1	1	1	1	$+\overline{V}+W$
POS = 0	(U + V +	- W). (U	$+\overline{V}+\overline{V}$	\overline{W}). $(\overline{U} + V + \overline{W})$
$\overline{U} + \overline{V}$	+ W)			1

Question 7:

Write the Sum of Product form of the function F(A, B, C) for the following truth table represen- tation of F.

Minterm

Α	В	С	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Answer:

A	B	С	F	SOP
0	0	0	0	
0	0	1	0	
0	1	0	1	A'BC'
0	1	1	1	A'BC
1	0	0	1	AB'C'
1	0	1	0	
1	1	0	0	
1	1	1	1	ABC

Question 8:

Write the SOP form of a boolean function F, which is represented in a truth table as follows:





x	Y.	z	F
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

F(X,Y,Z)=X'.Y'Z'+X'.Y'.Z'+X.Y'.Z'+X.Y.Z

Question 9:

Write the POS form of boolean function G, which is represented in a truth table as follows :

A	В	С	G
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Answer:

G(A,B,C)=(A+B+C).(A+B'+C').(A'+B+C).(A'+B+C')

Question 10:

Write the SOP form of a boolean function F, which is represented in a truth table as follows :

x	Y	z	F(X, Y, Z)
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1





SOP form is given by X'Y'Z' + X'Y'Z + X'YZ + XY'Z' + XYZ

Question 11:

Write the POS form of a boolean function F, which is represented in a truth table as follows :

A	В	С	F
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Answer:

(A + B + C). (A+B' + C').(A' + B + C').(A' + B' + C)

Question 12:

Write the SOP form of a boolean function F, which is represented in a truth table as follows :

U	v	W	F
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Answer:

F=(0,3,6,7)So, SOP form of F is F = (U'.V'W') + (U'.VW) + (U.VW') + (U.VW)

Question 13:

Write the POS form of a boolean function F, which is represented in a truth table as follows :





U	v	W	F
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

F(U, V, W) = (U + V + W').(U+V'+W').(U'+V+W')

Question 14:

Write the SOP form of a Boolean function F, which is represented in a truth table as follows :

Р	Q	R	G
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

x	у	z	Н
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Answer:

G(P, Q, R) = P'QR' + PQ'R' + PQR' + PQR

Question 15:

Write the POS form of a boolean function H, which is represented in a truth table as follows :

Answer:

H (x, y, z) = (x + y + z'). (x' + y + z'). (x' + y' + z)

Question 16:

Write the SOP form of a boolean function G, which is represented in a truth table as follows :

Р	Q	R	G
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1 .	1	1	1

Answer:

G(RQ, R) = P'QR' + P'QR + PQ'R' + PQR' + PQR

Question 17:

Write the POS form of a boolean function G, which is represented in a truth table as follows :

U	v	W	G
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Answer:

G(U. V W) = (U + V + W). (U' + V + W'). (U' + V' + W)

Question 18:

Write the POS form of a boolean function F, which is represented in a truth table as follows :

U	v	W	F
0	0	0	1

0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

Answer:

F(U, V, W) = (U + V + W') . (U + V' + W') . (U' + V + W')

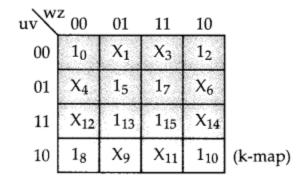
Question 19:

Reduse the following Boolean Expression to its simplest form using K-Map :

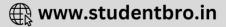
Answer:

Simplest expression is

= u v w z + u v w z + u v w z + u v w z + u v w z + u v w z + u v w z + u v w z + u v w z







Question 20:

Write the SOP form of a Boolean function G, which is represented in a truth table as

P	Q	R	G
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	e 0	1
1	0	· 1	0
1	1	0	1
1	1	1	1

follows :

Answer:

G(P, Q, R) = P'.Q.R' + PQ'.R' + EQ.R' + EQ.R

Question 21:

Write the POS equivalent of ABC + AB'C' + ABC'+ A'B'C

Answer:

 $\Sigma(1, 4, 5, 6, 7)$

 $POS = \pi (0, 2, 3)$

= (A + B + C) (A + B' + C). (A + B' + C')

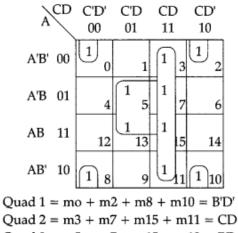
Short Answer Type questions-II

Question 1:

Obtain the minimal SOP form for the following Boolean expression using K-Map $F(A,B,C,D) = \Sigma (0,2,3,5,7,8,10,11,13,15)$





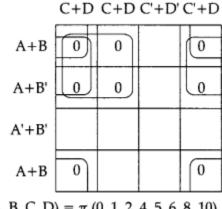


Quad 3 = m5 + m7 + m15 + m13 = BDMinimal SOP = B'D' + CD + BD

Question 2:

Reduce the following Boolean expression using K-Map: $F(A,B,C,D) = \pi(0,1,2,4,5,6,8,10)$

Answer:



$$\begin{split} F(A, B, C, D) &= \pi \ (0, 1, 2, 4, 5, 6, 8, 10) \\ F &= (A + C).(A + D).(B + D) \end{split}$$

Question 3:

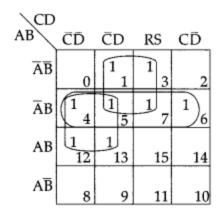
Reduce the following Boolean Expression using K-Map: Reduce the following using K-Map: $F(A, B,C,D) = \Sigma (1,3,4,5,6,7,12,13)$

Answer:



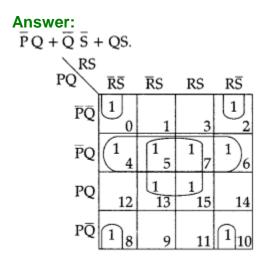


 $\overline{A} B + \overline{A} D + B \overline{C}$.



Question 4:

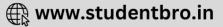
Reduce the following Boolean Expression using K-Map: $F(P,Q,R,S) = \Sigma(0,2,4,5,6,7,8,10,13,15)$.

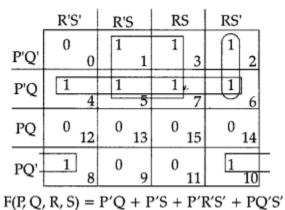


Question 5:

Reduce the following Boolean Expression using K-Map: $F(P, Q, R, S) = \Sigma (1, 2, 3, 4, 5, 6, 7, 8, 10)$



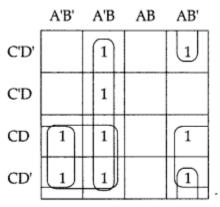


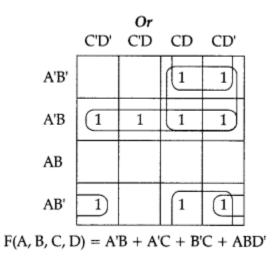


Question 6:

Reduce the following Boolean Expression using K-Map: $F(A, B, C, D) = \Sigma (2, 3, 4, 5, 6, 7, 8, 10, 11)$

Answer:





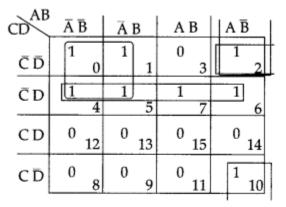




Question 7:

Reduce the following Boolean Expression using K-Map: $F(C, D, A, B) = \Sigma (0, 1, 2, 4, 5, 6, 7, 10)$

Answer:



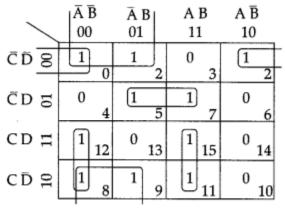
Quad 1 ($m_0 + m_1 + m_4 + m_5$) reduces to Quad 2 ($m_4 + m_5 + m_6 + m_7$) reduces to D Pair 1 ($m_2 + m_0$) reduces to A Hence, F(C, D, A, B) = + D + A

Question 8:

Reduce the following Boolean Expression using K-Map:

 $\mathbf{F}(\mathbf{C}, \mathbf{D}, \mathbf{A}, \mathbf{B}) = \Sigma (0, 1, 2, 5, 7, 8, 9, 11, 12, 15)$

Answer:



A'B'C + ABC + A'D' + B'C'D' + A'B'D

Question 9:

Reduce the following Boolean Expression using K-Map:

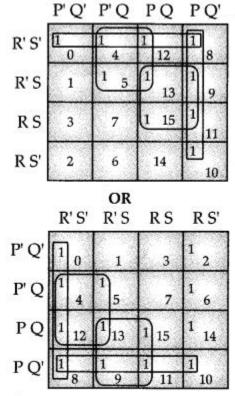




Reduce the following Expression to its simplest form using K-Map:

 $F(P, Q, R, S) = \Sigma(0, 4, 5, 8, 9, 10, 11, 12, 13, 15)$

Answer:

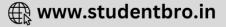


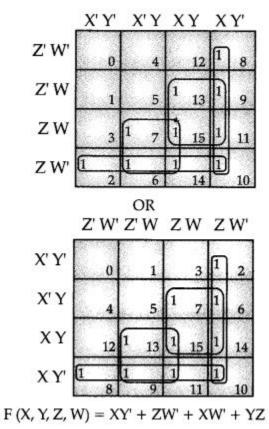
F(P, Q, R, S) = R'S' + PQ' + QR' + PS

Question 10:

Reduce the following Boolean Expression using K-Map: F (X, Y, Z, W) = Σ (2, 6, 7, 8, 9, 10, 11, 13, 14, 15)



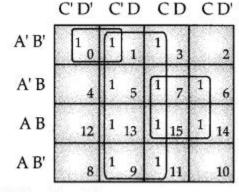




Question 11:

Reduce the following Boolean Expression using K-Map: F(A,B,C,D)=S(0,1,3,5,6,7,11,13,14,15)

Answer:



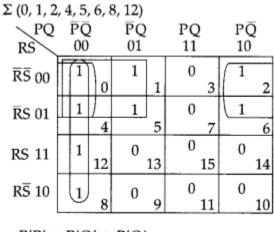
A'B'C' + D + BC

Question 12:

Reduce the following Boolean Expression using K-Map: $F(R, S, P, Q) = \Sigma (0, 1, 2, 4, 5, 6, 8, 12)$





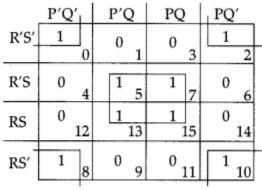


= P'R' + P'Q' + R'Q'

Question 13:

Simplify the following boolean expression using K-Map: $F(R, S, P, Q) = \Sigma(0, 2, 5, 7, 8, 10, 13, 15)$



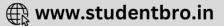


F(A, B, C, D) = Q'S' + QS

Question 14:

Reduce the following boolean expression using K-Map: $F(U, V, W,Z) = \Sigma (0, 1, 2, 4, 5, 6, 8, 10)$



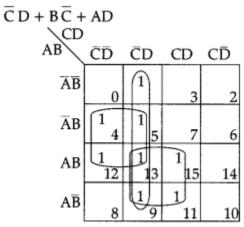


$F(W, Z, U, V) = \Sigma(0,1, 2, 4, 5, 6, 8, 10)$					
	U'V'	U'V	UV	UV'	
W'Z'	1	1	0	1	
		1	3	2	
W'Z-	1	1	0	1	
	4	5	7	6	
WZ	0	0	0	0	
	12	13	15	14	
WZ′	1	0	0	1	
	- 8	9	11	10	
F(U, V, W, Z) = U'W' + V'Z' + W'V'					

Question 15:

Reduce the following expression using K-map : $F(A,B,C,D) = \Sigma (1,4,5,9,11,12,13,15).$

Answer:

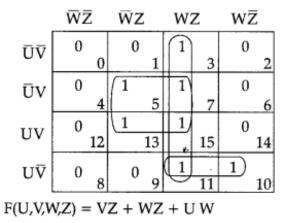


Question 16:

Reduce the following Boolean Expression using K-Map: $F(U, \overline{V}, W, Z) = \Sigma (3, 5, 7, 10, 11, 13, 15)$







Question 17:

Reduce the follwing Boolean Expression using K-Map: F (A, B, C, D,) = Σ (3, 4, 5, 6, 7, 13, 15)

Answer:

